

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (currently amended): A frequency dividing circuit comprising:

a first frequency divider for dividing output of a local oscillator and outputting a first in-phase local oscillation signal and a first quadrature local oscillation signal;

a second frequency divider being connected to an output side of the first in-phase local oscillation signal output for dividing the first in-phase local oscillation signal and outputting a second in-phase local oscillation signal and a second quadrature local oscillation signal; and

a dummy circuit being connected to the first quadrature local oscillation signal output and having input impedance equal to that of the second frequency divider~~a phase correction unit which keeps the phase difference between the first in-phase local oscillation signal and the first quadrature local oscillation signal at 90 degrees.~~

Claims 2 to 4 (canceled)

Claim 5 (currently amended): The frequency dividing circuit according to claim ~~[[3]]~~1, wherein the dummy circuit is a circuit including a resistor and a capacitor.

Claim 6 (currently amended): The frequency dividing circuit according to claim ~~[[3]]~~1, wherein the dummy circuit is the same amplifier as an input amplifier of the second frequency divider.

Claim 7 (currently amended): The frequency dividing circuit according to claim ~~[[3]]~~1, wherein the dummy circuit is the same circuit as a part of an input amplifier of the second frequency divider.

Claims 8 to 22 (canceled)

Claim 23 (previously presented): A frequency dividing circuit comprising:

a first frequency divider for dividing output of a local oscillator and outputting a first in-phase local oscillation signal and a first quadrature local oscillation signal;

a second frequency divider being connected to an output side of the first quadrature local oscillation signal output for dividing the first quadrature local oscillation signal and outputting a second in-phase local oscillation signal and a second quadrature local oscillation signal; and

a dummy circuit being connected to the first in-phase local oscillation signal output and having input impedance equal to that of the second frequency divider which is in a turned-off

~~state a phase correction unit which keeps the phase difference between the first in-phase local oscillation signal and the first quadrature local oscillation signal at 90 degrees.~~

Claim 24 (canceled)

Claim 25 (currently amended): The frequency dividing circuit according to claim ~~[[24]]~~23, wherein the dummy circuit is a circuit including a resistor and a capacitor.

Claim 26 (currently amended): The frequency dividing circuit according to claim ~~[[24]]~~23, wherein the dummy circuit is the same amplifier as an input amplifier of the second frequency divider.

Claim 27 (currently amended): The frequency dividing circuit according to claim ~~[[24]]~~23, wherein the dummy circuit is the same circuit as a part of an input amplifier of the second frequency divider.

Claim 28 (previously presented): The frequency dividing circuit according to claim 26, further comprising a control section for controlling the current of the input amplifier and the dummy circuit.

Claims 29 to 41 (canceled)

Claim 42 (new): The frequency dividing circuit according to claim 1, further comprising a control section which is connected to the second frequency divider and turns on and off the second frequency divider.

Claim 43 (new): The frequency dividing circuit according to claim 6, further comprising a control section which is connected to input amplifier of the second frequency divider and turns on and off the input amplifier of the second frequency divider.

Claim 44 (new): The frequency dividing circuit according to claim 7, further comprising a control section which is connected to input amplifier of the second frequency divider and turns on and off the input amplifier of the second frequency divider.

Claim 45 (new): The frequency dividing circuit according to claim 23, further comprising a control section which is connected to the second frequency divider and turns on and off the second frequency divider.

Claim 46 (new): The frequency dividing circuit according to claim 26, further comprising a control section which is connected to input amplifier of the second frequency divider and

turns on and off the input amplifier of the second frequency divider.

Claim 47 (new): The frequency dividing circuit according to claim 27, further comprising a control section which is connected to input amplifier of the second frequency divider and turns on and off the input amplifier of the second frequency divider.